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Nanotechnology, 2003. IEEE-NANO 2003. 2003 Third IEEE Conference on , Vol 12-14 Aug. 2003

Page(s): 469 -472

[\[Abstract\]](#) [\[PDF Full-Text \(389 KB\)\]](#) **IEEE CNF****2 Compiler-directed customization of ASIP cores***Gupta, T.V.K.; Ko, R.E.; Barua, R.;*

Hardware/Software Codesign, 2002. CODES 2002. Proceedings of the Tenth International Symposium on , 6-8 May 2002

Page(s): 97 -102

[\[Abstract\]](#) [\[PDF Full-Text \(499 KB\)\]](#) **IEEE CNF****3 RbGd/sub 2/Br/sub 7/:Ce scintillators for gamma-ray and thermal n detection***Shah, K.S.; Cirignano, L.; Grazioso, R.; Klugerman, M.; Bennett, P.R.; Gupta, Moses, W.W.; Weber, M.J.; Derenzo, S.E.;*

Nuclear Science, IEEE Transactions on , Volume: 49 Issue: 4 , Aug. 2002

Page(s): 1655 -1660

[\[Abstract\]](#) [\[PDF Full-Text \(242 KB\)\]](#) **IEEE JNL****4 Processor evaluation in an embedded systems design environment***Gupta, T.V.K.; Sharma, P.; Balakrishnan, M.; Malik, S.;*

VLSI Design, 2000. Thirteenth International Conference on , 3-7 Jan. 2000

Page(s): 98 -103

[\[Abstract\]](#) [\[PDF Full-Text \(44 KB\)\]](#) **IEEE CNF****5 High speed X-ray imaging camera using a structured CsI(Tl) scintillator***Nagarkar, V.V.; Tipnis, S.V.; Gupta, T.K.; Miller, S.R.; Gaysinskiy, V.B.; Kluge Y.; Squillante, M.R.; Entine, G.; Moses, W.W.;*

Nuclear Science, IEEE Transactions on , Volume: 46 Issue: 3 , June 1999
Page(s): 232 -236

[\[Abstract\]](#) [\[PDF Full-Text \(692 KB\)\]](#) **IEEE JNL**

6 High speed X-ray imaging camera using structured CsI(Tl) scintillato
*Nagarkar, V.V.; Tipnis, S.V.; Gupta, T.K.; Miller, S.; Gaysinskiy, V.; Klugerman
Squillante, M.R.; Entine, G.; Moses, W.W.;*
Nuclear Science Symposium, 1998. Conference Record. 1998 IEEE , Volume: 1
Nov. 1998
Page(s): 158 -162 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(316 KB\)\]](#) **IEEE CNF**

7 Structured CsI(Tl) scintillators for X-ray imaging applications
Nagarkar, V.V.; Gupta, T.K.; Miller, S.R.; Klugerman, Y.; Squillante, M.R.; Enti
Nuclear Science, IEEE Transactions on , Volume: 45 Issue: 3 , June 1998
Page(s): 492 -496

[\[Abstract\]](#) [\[PDF Full-Text \(564 KB\)\]](#) **IEEE JNL**

8 Structured CsI(Tl) scintillators for X-ray imaging applications
Nagarkar, V.V.; Gupta, T.K.; Miller, S.; Klugarman, Y.; Squillante, M.R.; Entine
Nuclear Science Symposium, 1997. IEEE , 9-15 Nov. 1997
Page(s): 226 -230 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(512 KB\)\]](#) **IEEE CNF**

9 CCD-based high resolution digital radiography system for non destru
evaluation
Nagarkar, V.V.; Gordon, J.S.; Gupta, T.K.; Vasile, S.; Gothoskar, P.; Squillante
Entine, G.;
Nuclear Science, IEEE Transactions on , Volume: 44 Issue: 3 , June 1997
Page(s): 885 -889

[\[Abstract\]](#) [\[PDF Full-Text \(768 KB\)\]](#) **IEEE JNL**

10 Implementation of a fuzzy controller for DC-DC converters using an
inexpensive 8-b microcontroller
Gupta, T.; Boudreaux, R.R.; Nelms, R.M.; Hung, J.Y.;
Industrial Electronics, IEEE Transactions on , Volume: 44 Issue: 5 , Oct. 1997
Page(s): 661 -669

[\[Abstract\]](#) [\[PDF Full-Text \(196 KB\)\]](#) **IEEE JNL**

11 CCD-based high resolution digital radiography system for nondestru
evaluation
Nagarkar, V.V.; Gordon, J.S.; Gupta, T.K.; Vasile, S.; Gothoskar, P.; Entine, G.
Nuclear Science Symposium, 1996. Conference Record., 1996 IEEE , Volume: 1

Nov. 1996

Page(s): 246 -250 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(688 KB\)\]](#) **IEEE CNF**

12 Effect of gallium oxide on crystallization and thermal expansion behavior of low k glass composite

Jan-Ho Jean; Gupta, T.K.;

Components, Packaging, and Manufacturing Technology, Part A, IEEE Transactions on [see also Components, Hybrids, and Manufacturing Technology, IEEE Transactions on], Volume: 18 Issue: 2, June 1995

Page(s): 438 -443

[\[Abstract\]](#) [\[PDF Full-Text \(612 KB\)\]](#) **IEEE JNL**

13 Design of low dielectric glass+ceramics for multilayer ceramic substrates

Jean, J.-H.; Gupta, T.K.;

Components, Packaging, and Manufacturing Technology, Part B: Advanced Packaging Technology, IEEE Transactions on [see also Components, Hybrids, and Manufacturing Technology, IEEE Transactions on], Volume: 17 Issue: 2, May 1994

Page(s): 228 -233

[\[Abstract\]](#) [\[PDF Full-Text \(552 KB\)\]](#) **IEEE JNL**

14 Evaluation of die-attach adhesives by curvature measurements

Lu, G.-Q.; Kromann, G.B.; Mogilevsky, B.; Gupta, T.K.;

Thermal Phenomena in Electronic Systems, 1992. I-THERM III, InterSociety Conference on, 5-8 Feb. 1992

Page(s): 155 -158

[\[Abstract\]](#) [\[PDF Full-Text \(276 KB\)\]](#) **IEEE CNF**

15 Capacity planning in wood products industry using simulation

Gupta, T.; Arasakesari, S.;

Simulation Conference, 1991. Proceedings., Winter, 8-11 Dec. 1991

Page(s): 435 -440

[\[Abstract\]](#) [\[PDF Full-Text \(424 KB\)\]](#) **IEEE CNF**

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Ammer, M.J.; Sheets, M.; Karalar, T.; Kuulusa, M.; Rabaey, J.;
Design Automation Conference, 2003. Proceedings, June 2-6, 2003
Page(s): 916 -919

[\[Abstract\]](#) [\[PDF Full-Text \(662 KB\)\]](#) **IEEE CNF****2 Flexible implementation of a WCDMA Rake receiver**

Harju, L.; Kuulusa, M.; Nurmi, J.;
Signal Processing Systems, 2002. (SIPS '02). IEEE Workshop on, 16-18 Oct. 2
Page(s): 177 -182

[\[Abstract\]](#) [\[PDF Full-Text \(588 KB\)\]](#) **IEEE CNF****3 Advanced instruction set architectures for reducing program memory in a DSP processor**

Simonen, P.; Saastamoinen, K.; Kuulusa, M.; Nurmi, J.;
Electronic Design, Test and Applications, 2002. Proceedings. The First IEEE International Workshop on, 29-31 Jan. 2002
Page(s): 477 -479

[\[Abstract\]](#) [\[PDF Full-Text \(243 KB\)\]](#) **IEEE CNF****4 Comparison of static logic styles for low-voltage digital design**

Kontiala, M.; Kuulusa, M.; Nurmi, J.;
Electronics, Circuits and Systems, 2001. ICECS 2001. The 8th IEEE International Conference on, Volume: 3, 2-5 Sept. 2001
Page(s): 1421 -1424 vol.3

[\[Abstract\]](#) [\[PDF Full-Text \(304 KB\)\]](#) **IEEE CNF****5 A flexible RAKE receiver architecture for WCDMA mobile terminals**

Harju, L.; Kuulusa, M.; Nurmi, J.;
Wireless Communications, 2001. (SPAWC '01). 2001 IEEE Third Workshop on Processing Advances in, 20-23 March 2001

Page(s): 9 -12

[\[Abstract\]](#) [\[PDF Full-Text \(344 KB\)\]](#) [IEEE CNF](#)

6 Comparison of bulk and SOI CMOS technologies in a DSP processor core implementation

Simonen, P.; Heinonen, A.; Kuulusa, M.; Nurmi, J.;

Microelectronics, 2001. ICM 2001 Proceedings. The 13th International Conference on, Oct. 29-31, 2001

Page(s): 107 -110

[\[Abstract\]](#) [\[PDF Full-Text \(608 KB\)\]](#) [IEEE CNF](#)

7 Enhanced DSP core for embedded applications

Takala, J.; Kuulusa, M.; Ojala, P.; Nurmi, J.;

Signal Processing Systems, 1999. SiPS 99. 1999 IEEE Workshop on, 20-22 Oct. 1999

Page(s): 271 -280

[\[Abstract\]](#) [\[PDF Full-Text \(532 KB\)\]](#) [IEEE CNF](#)

8 Run-time configurable hardware model in a dataflow simulation [modeling and simulation]

Kuulusa, M.; Takala, J.; Saarinen, J.;

Circuits and Systems, 1998. IEEE APCCAS 1998. The 1998 IEEE Asia-Pacific Conference on, 24-27 Nov. 1998

Page(s): 763 -766

[\[Abstract\]](#) [\[PDF Full-Text \(392 KB\)\]](#) [IEEE CNF](#)

9 A flexible DSP core for embedded systems

Kuulusa, M.; Nurmi, J.; Takala, J.; Ojala, P.; Herranen, H.;

Design & Test of Computers, IEEE, Volume: 14 Issue: 4, Oct.-Dec. 1997

Page(s): 60 -68

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Publisher

ACM Press New York, NY, USA

Pages: 246 - 251 Series-Proceeding-Article
Year of Publication: 1997
ISBN:0-89791-920-3

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